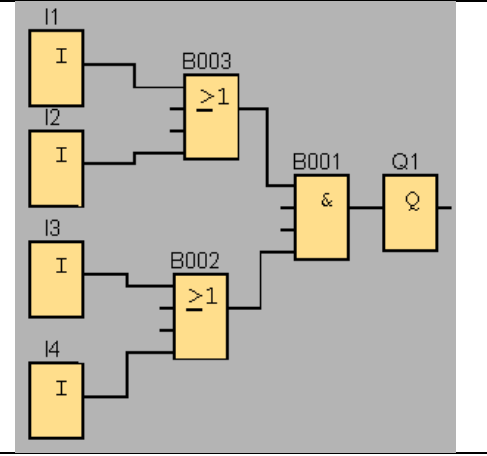
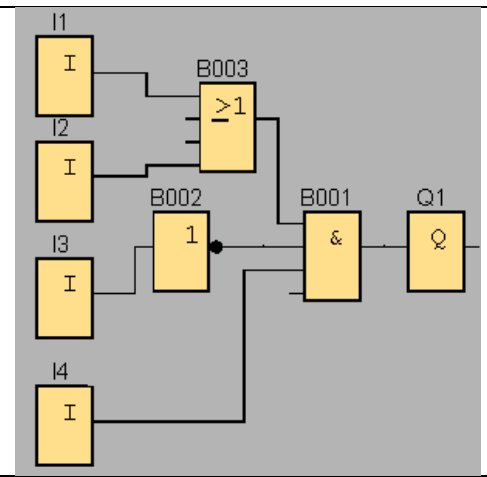
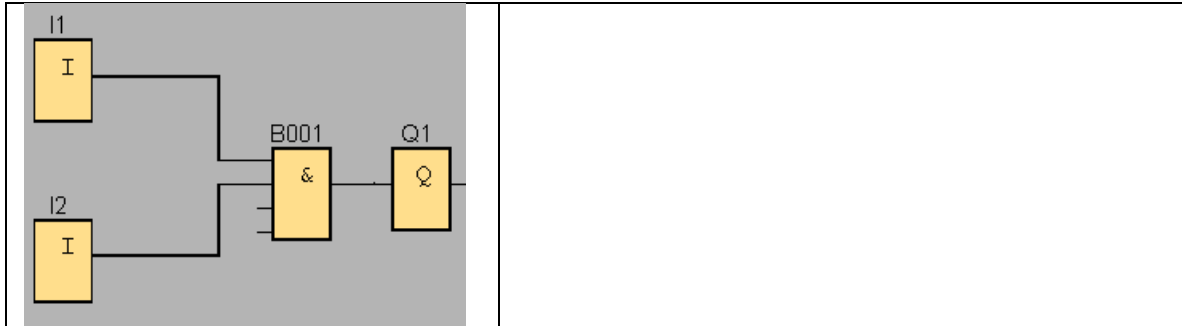


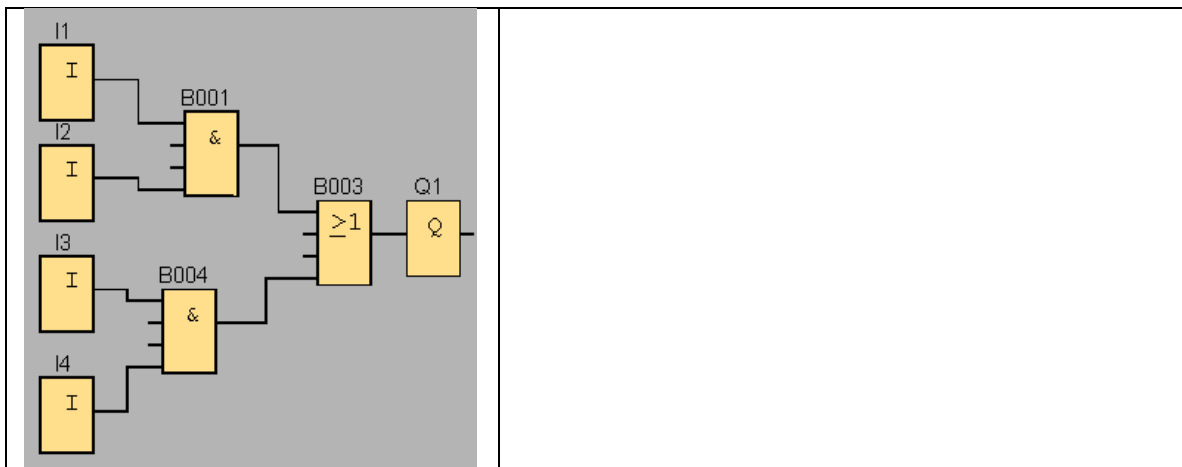
Q1. Write the expression for each one of the following FBDs and draw the LAD:

	
<p>Expression:</p>	

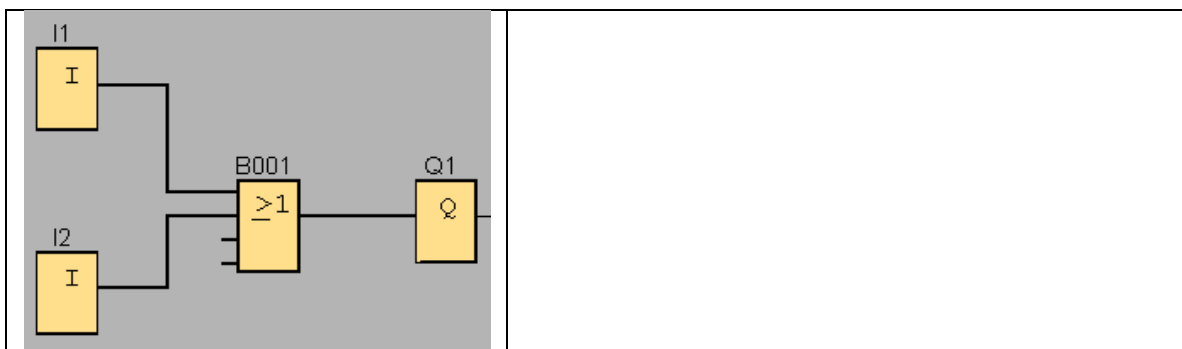
	
<p>Expression:</p>	



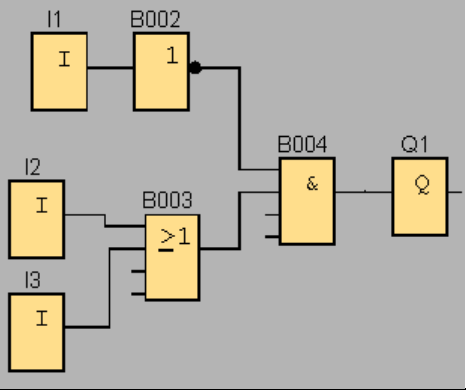
Expression:

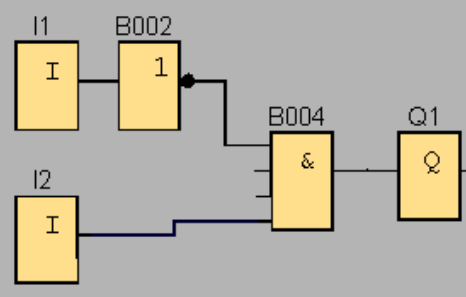


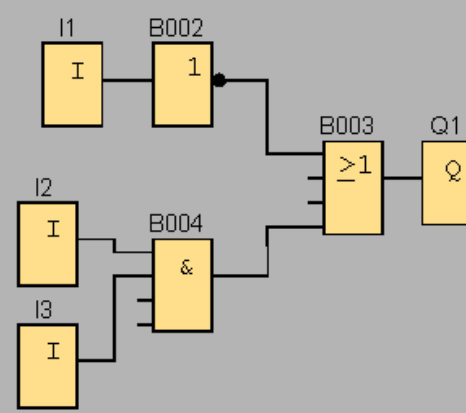
Expression:

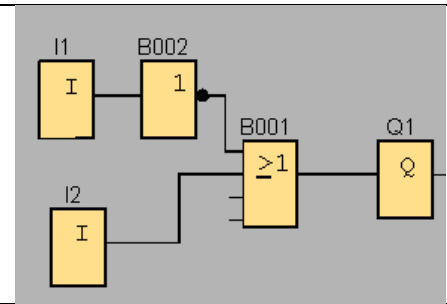


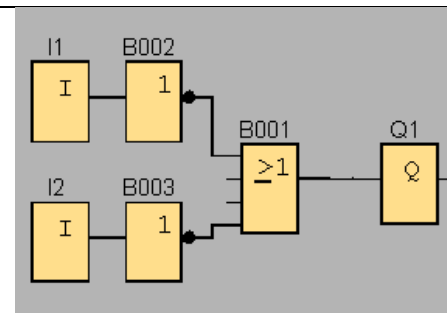
Expression:

 <p>The diagram shows a PLC ladder logic circuit. It starts with input I1 connected to a normally open contact labeled B002 with a '1' inside. This contact is in series with a normally open contact labeled B003 with a '&gt;1' inside. The output of this series combination is connected to an AND gate (labeled B004 with an '&amp;' symbol). Another input to the AND gate is from input I2. The output of the AND gate is connected to output Q1 (labeled Q1 with a 'Q' symbol). Input I3 is also connected to the AND gate but has no connection to the output.</p>	
Expression:	

 <p>The diagram shows a PLC ladder logic circuit. It starts with input I1 connected to a normally open contact labeled B002 with a '1' inside. This contact is in series with a normally open contact labeled B004 with an '&amp;' symbol. The output of this series combination is connected to output Q1 (labeled Q1 with a 'Q' symbol). Input I2 is also connected to the AND gate but has no connection to the output.</p>	
Expression:	

 <p>The diagram shows a PLC ladder logic circuit. It starts with input I1 connected to a normally open contact labeled B002 with a '1' inside. This contact is in series with a normally open contact labeled B003 with a '&gt;1' inside. The output of this series combination is connected to an AND gate (labeled B004 with an '&amp;' symbol). Another input to the AND gate is from input I2. The output of the AND gate is connected to output Q1 (labeled Q1 with a 'Q' symbol). Input I3 is also connected to the AND gate but has no connection to the output.</p>	
Expression:	

	
Expression:	

	
Expression:	

**Answers**